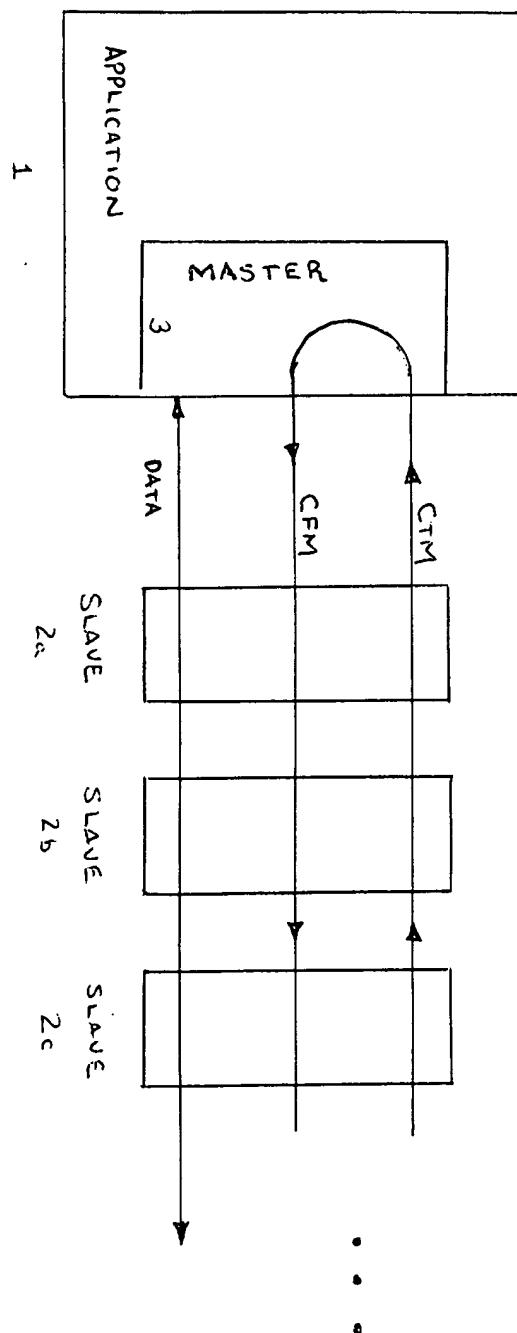


Figure 1A



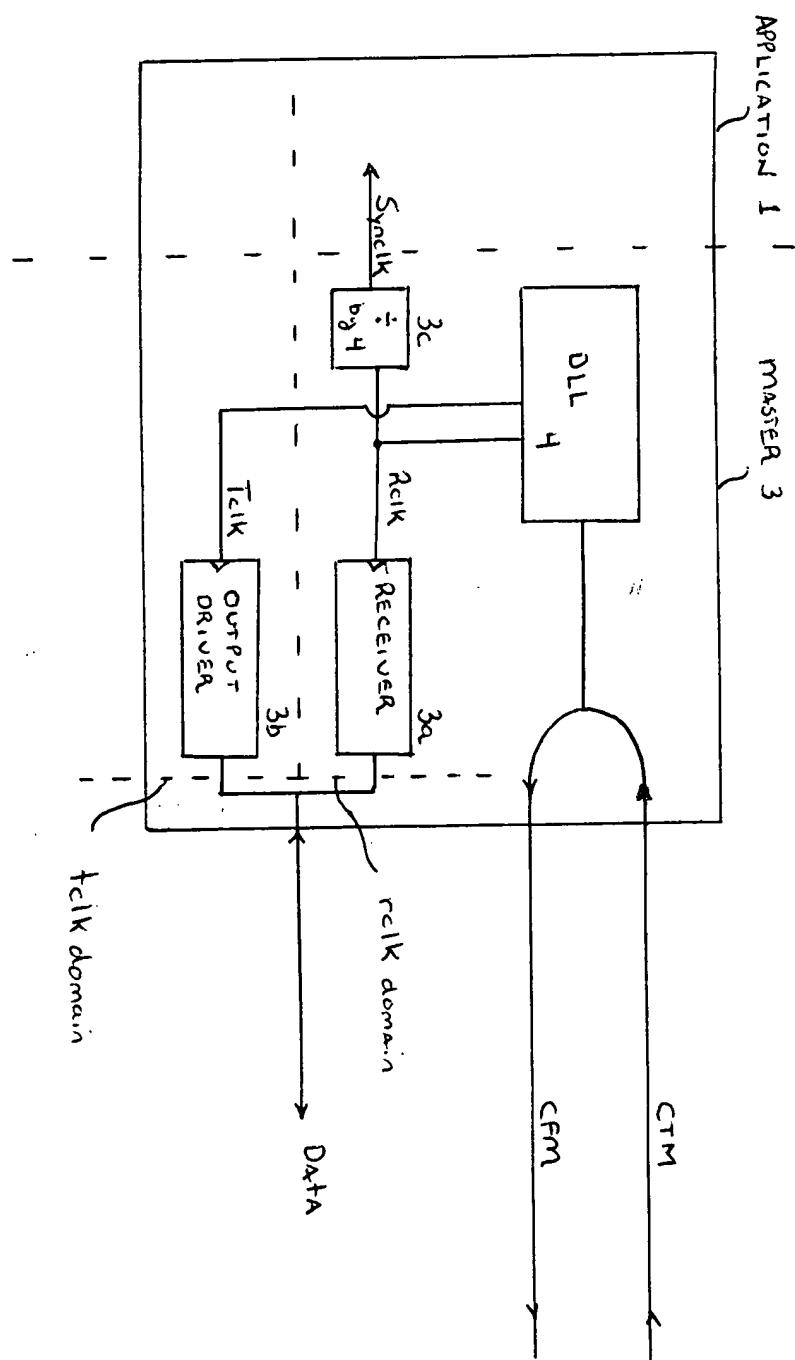


Figure 1B

Figure 2

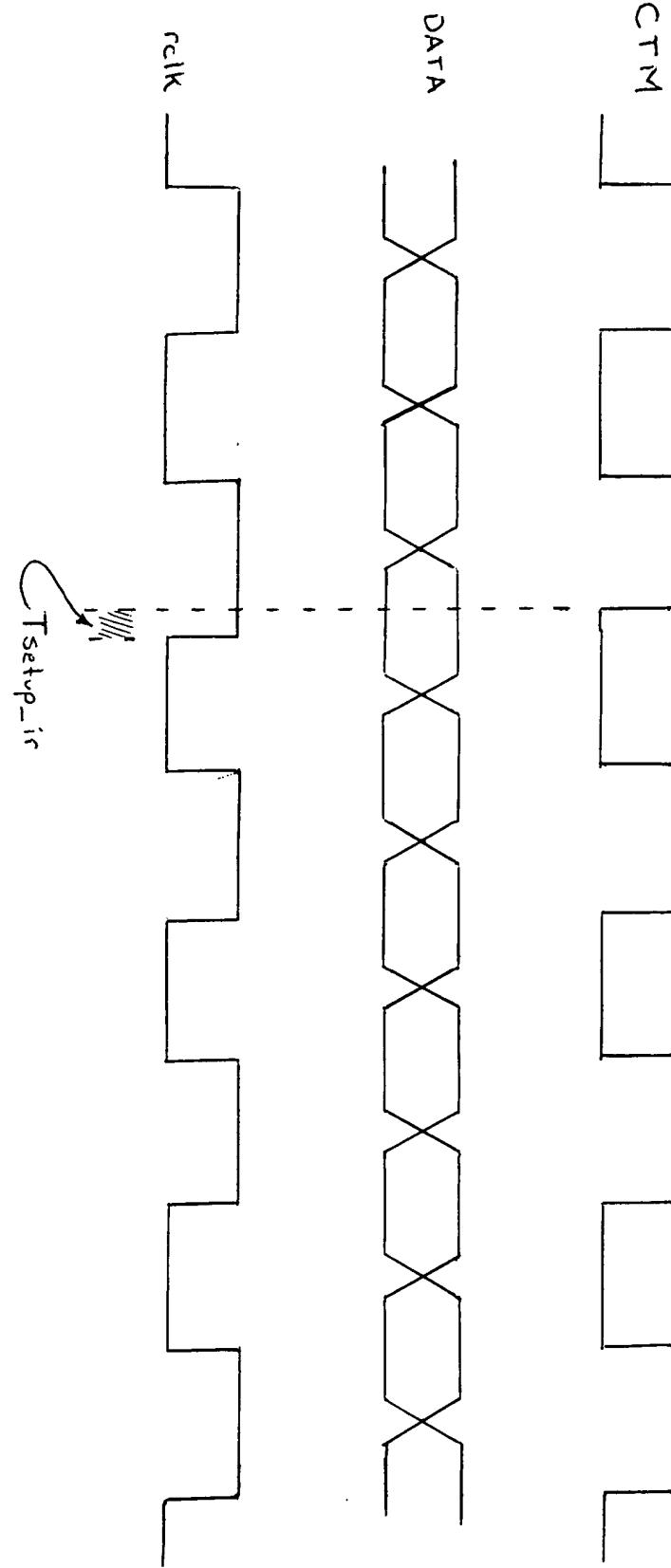




Figure 3

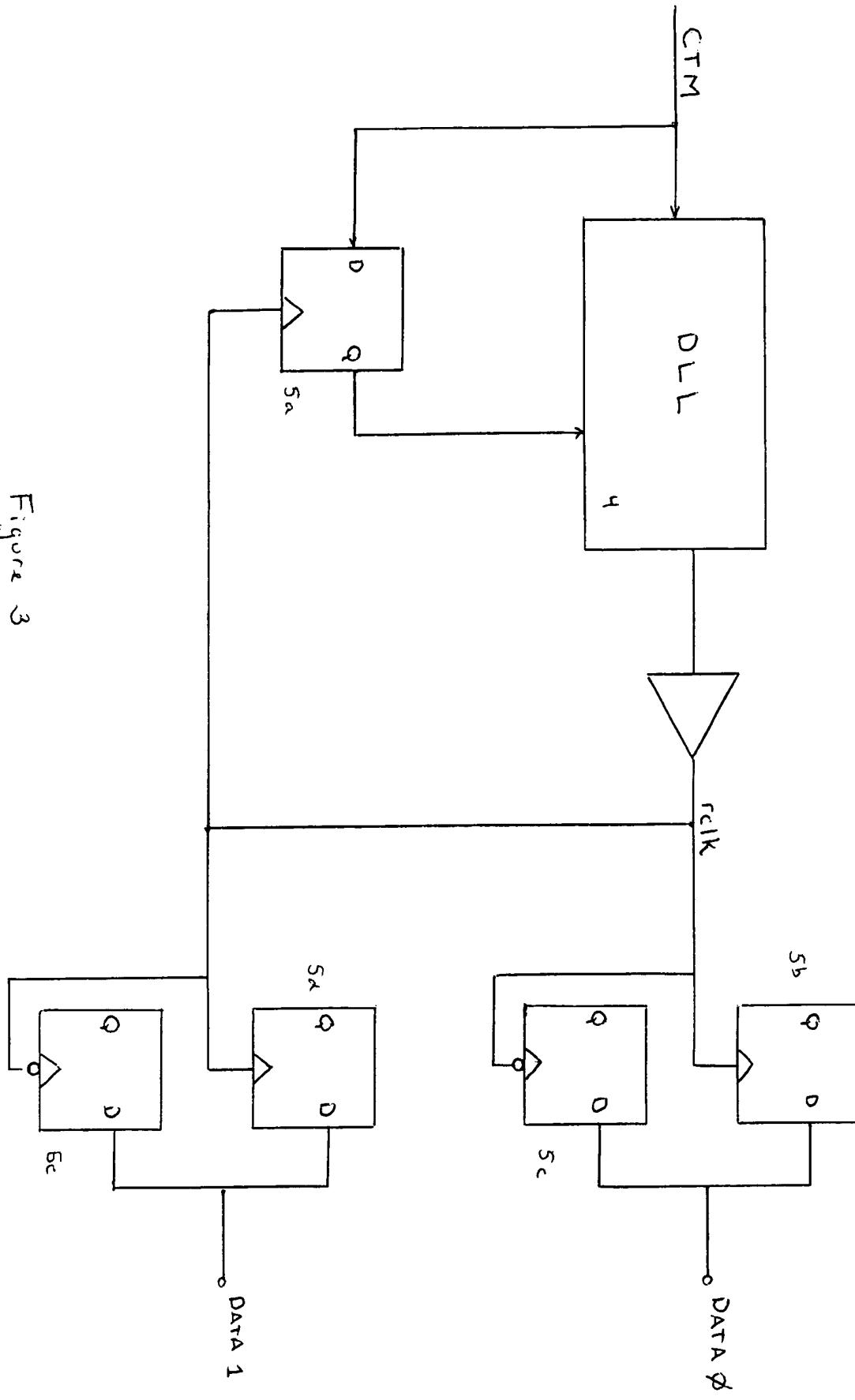


Figure 4

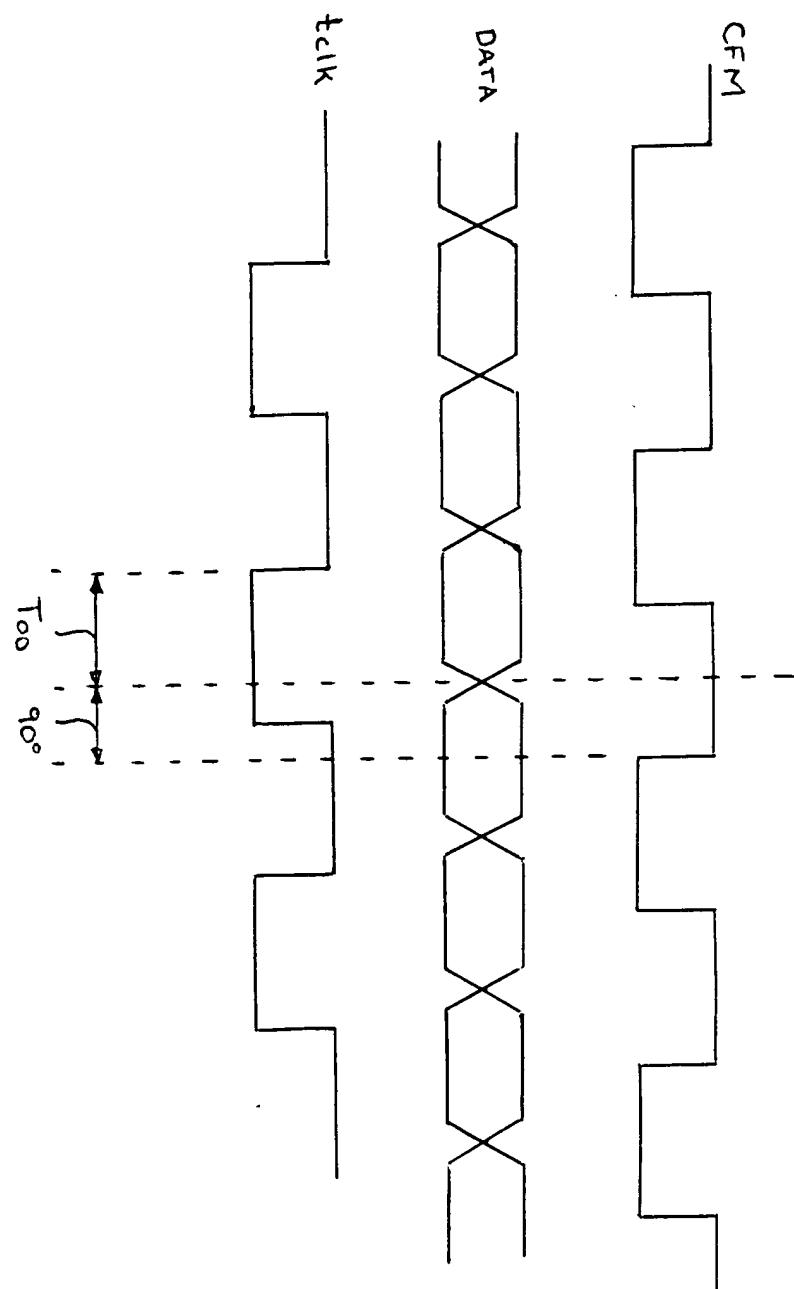


Figure 5

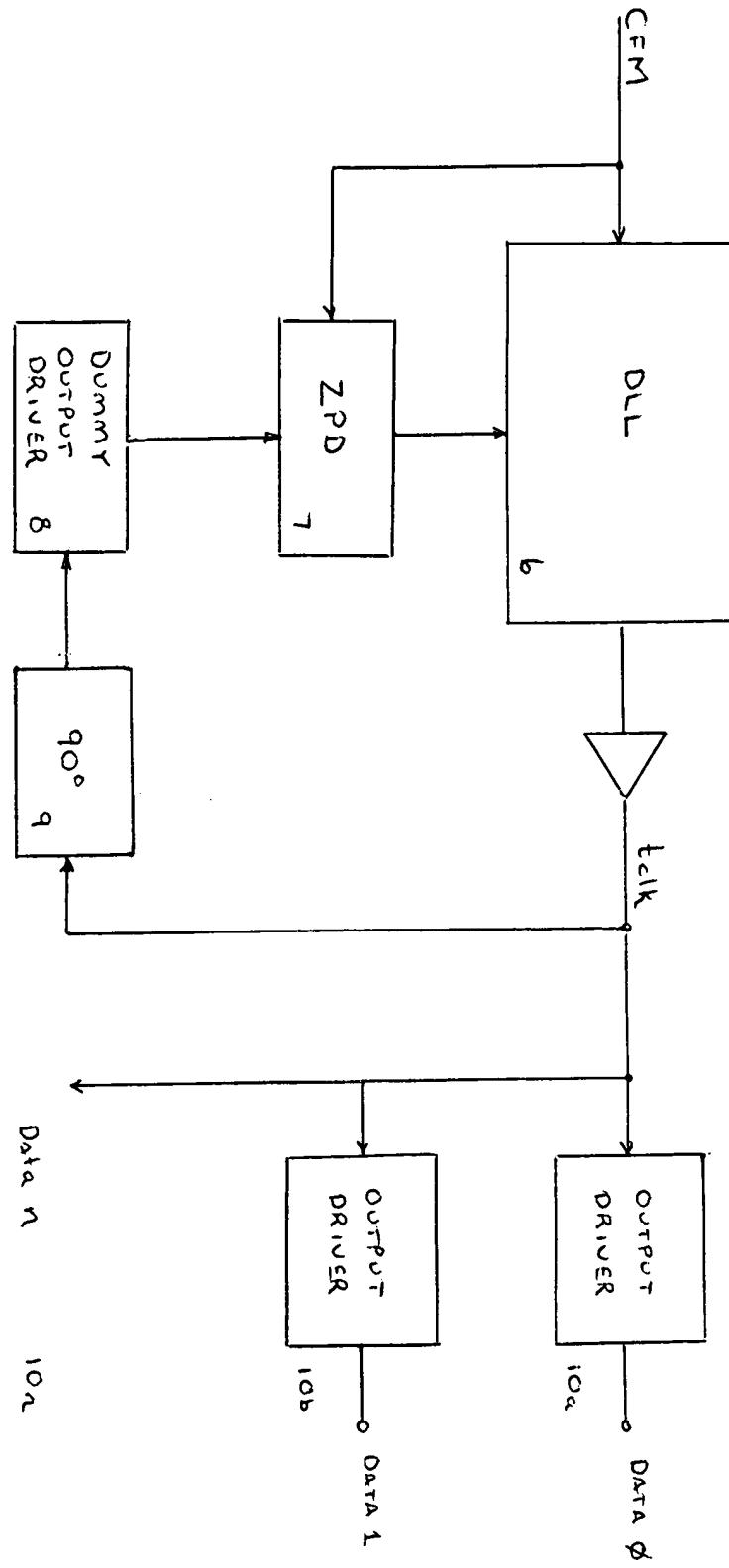
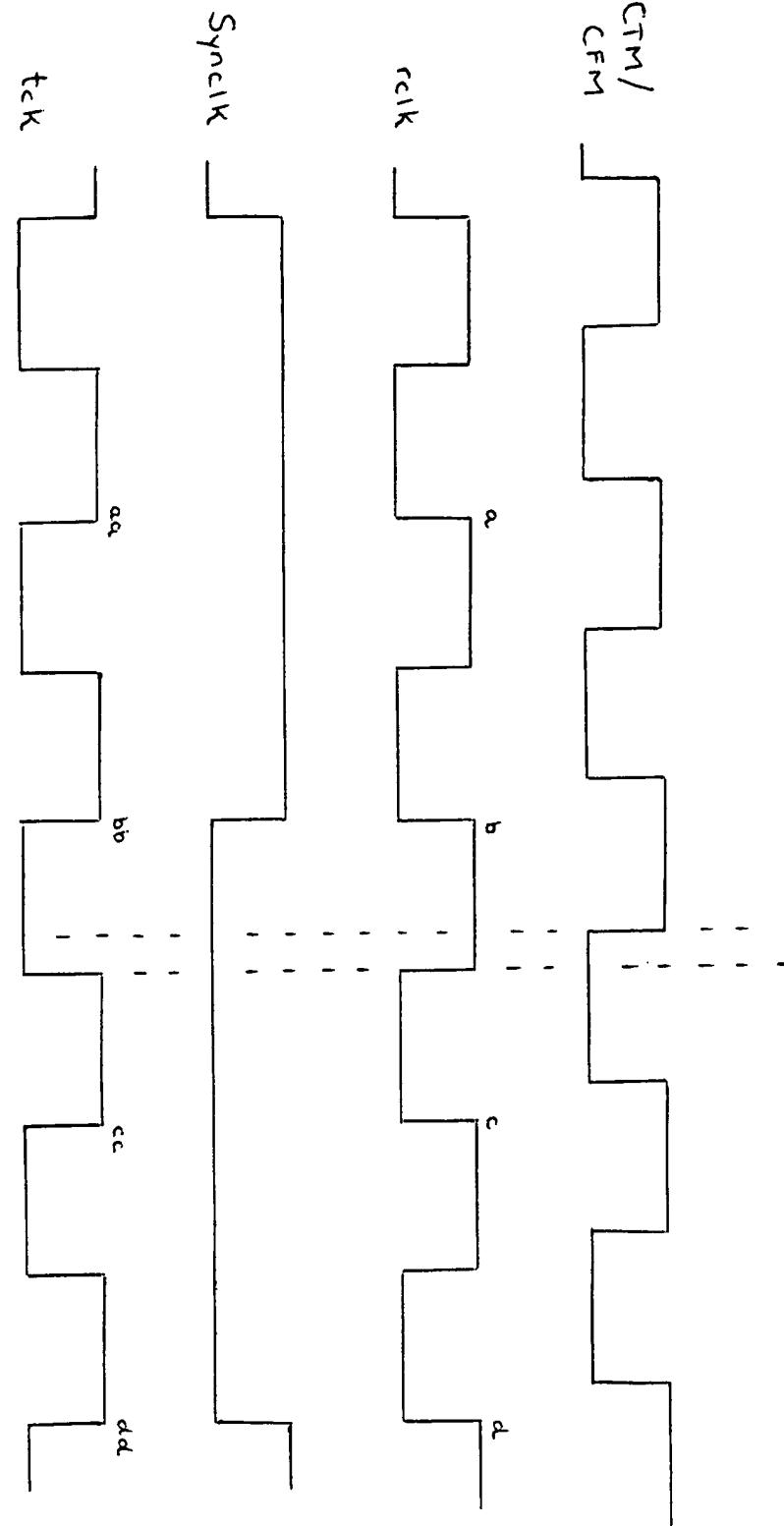


Figure 6

$90^\circ \rightarrow T_{od}$, where ($T_{od} = 90^\circ - T_{seto-1a}$)



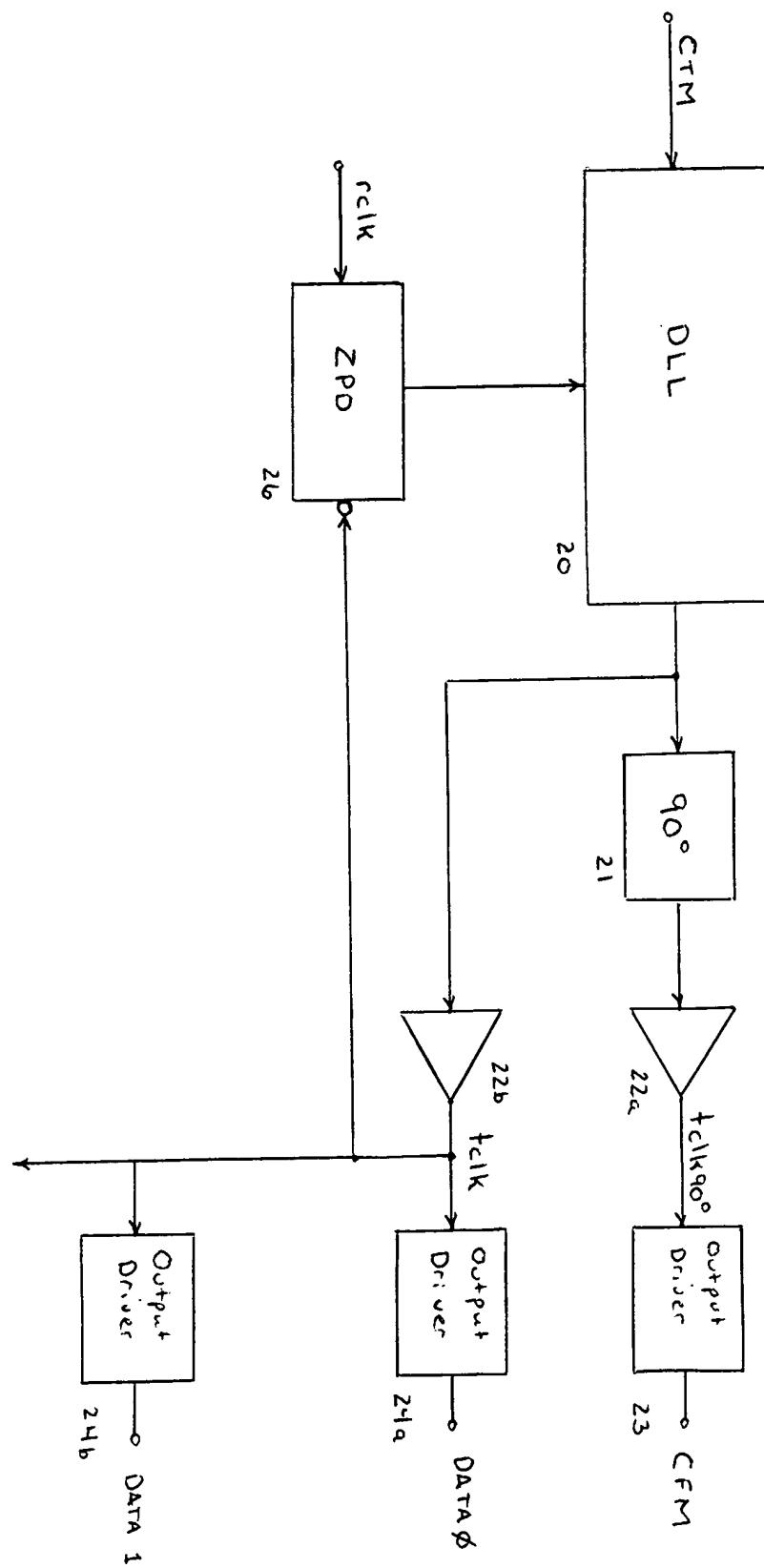
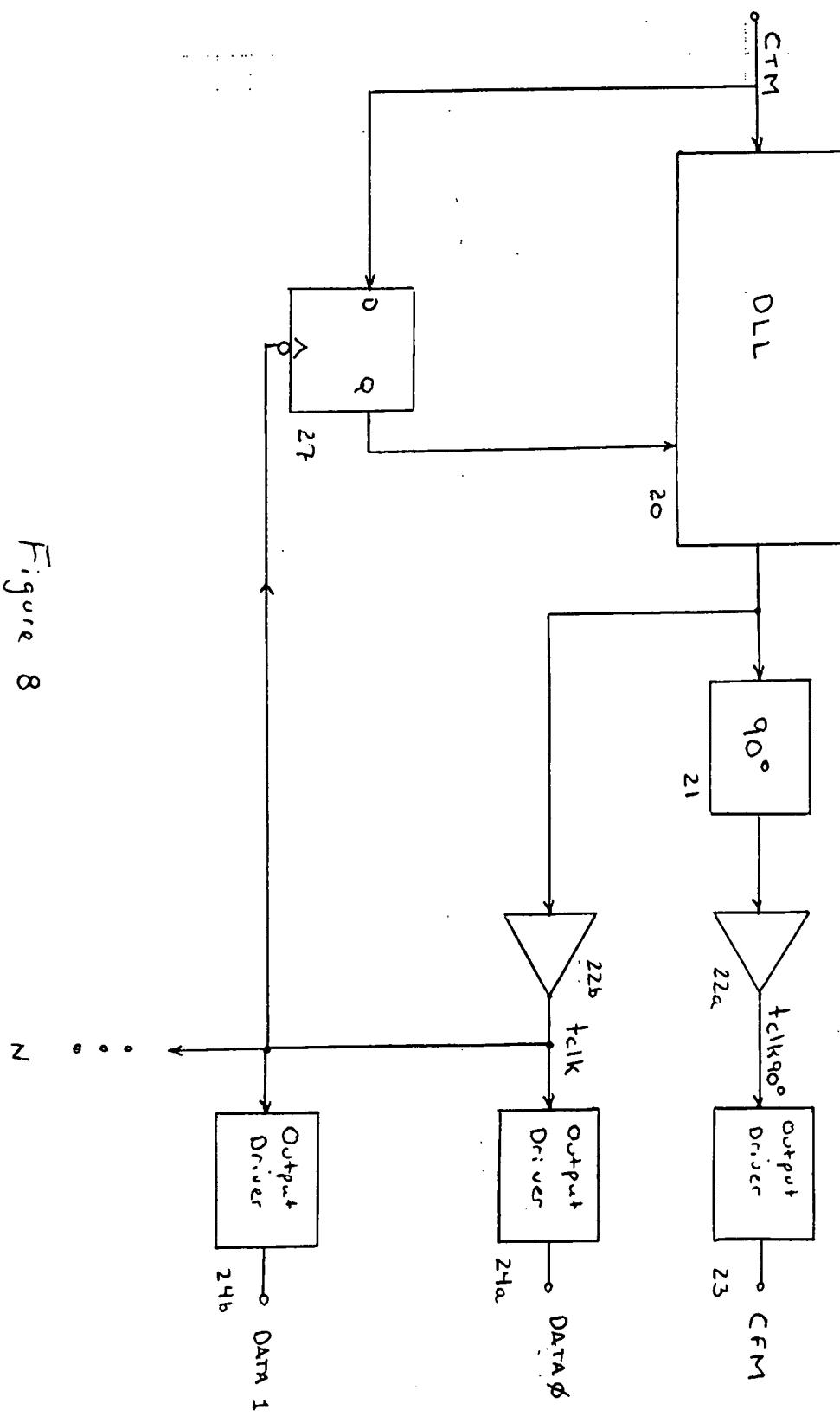




Figure 8



relk/tclk aligned at 180°

Input receiver setup time

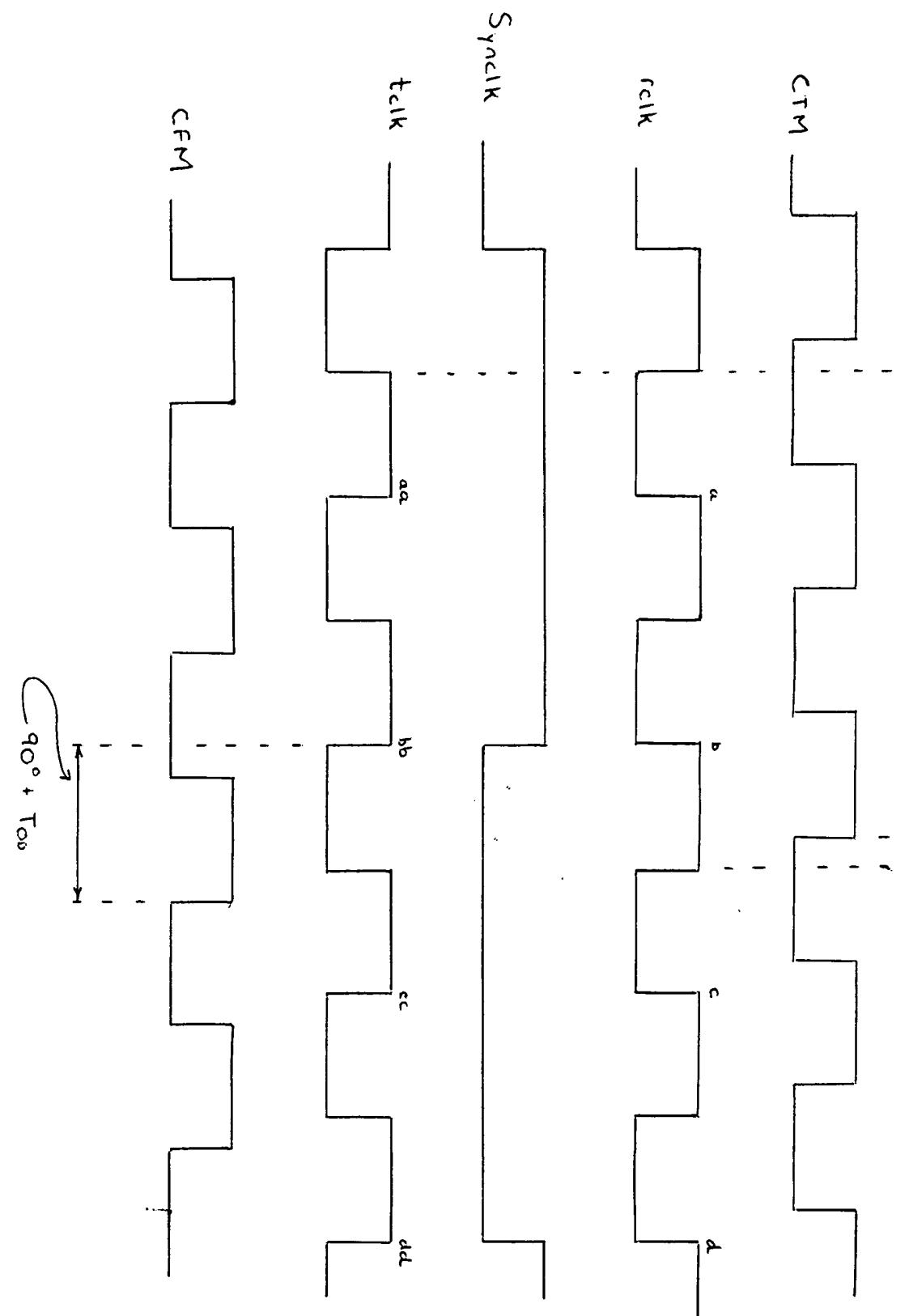


Figure 9

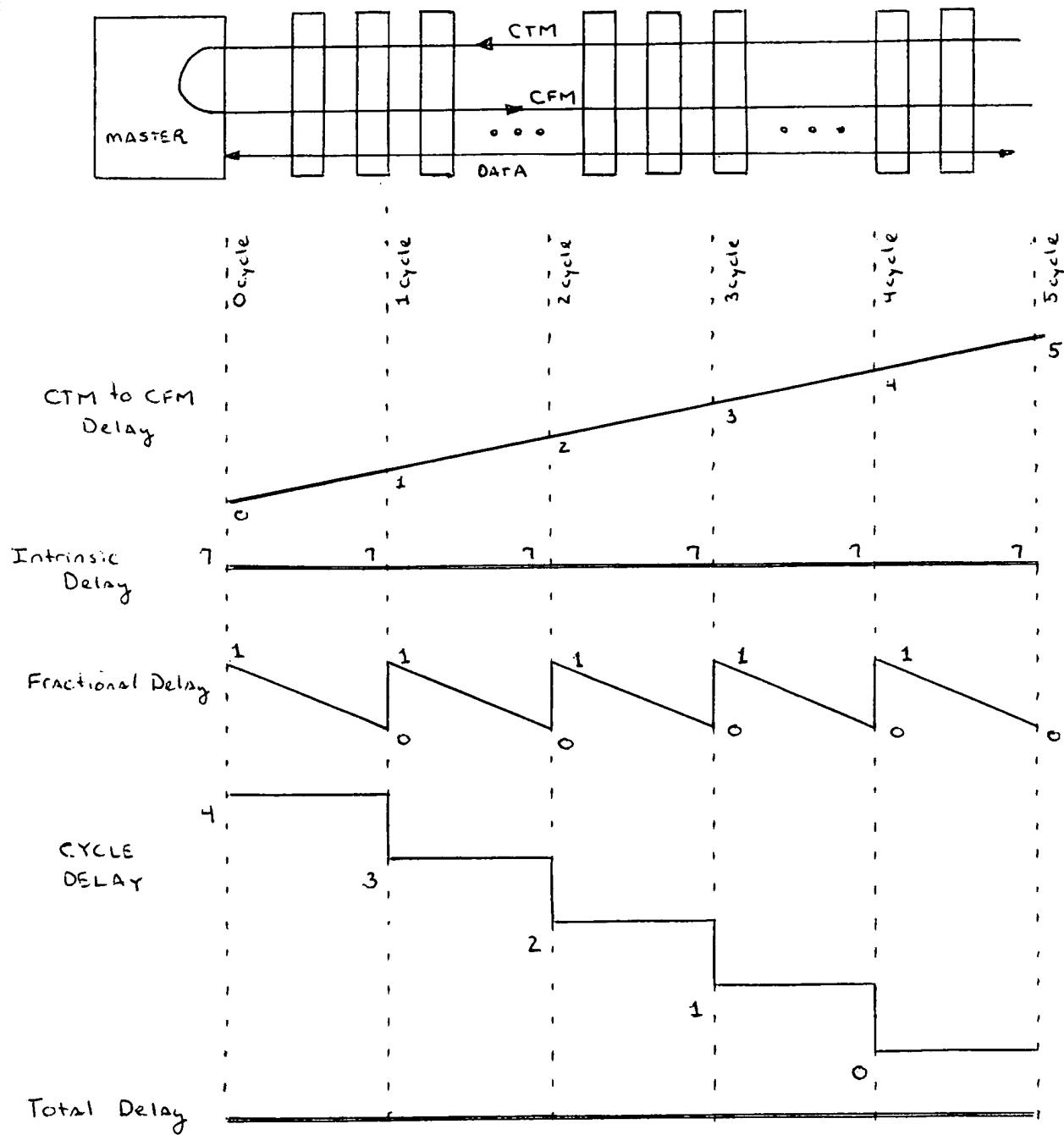


Figure 18

Figure 11

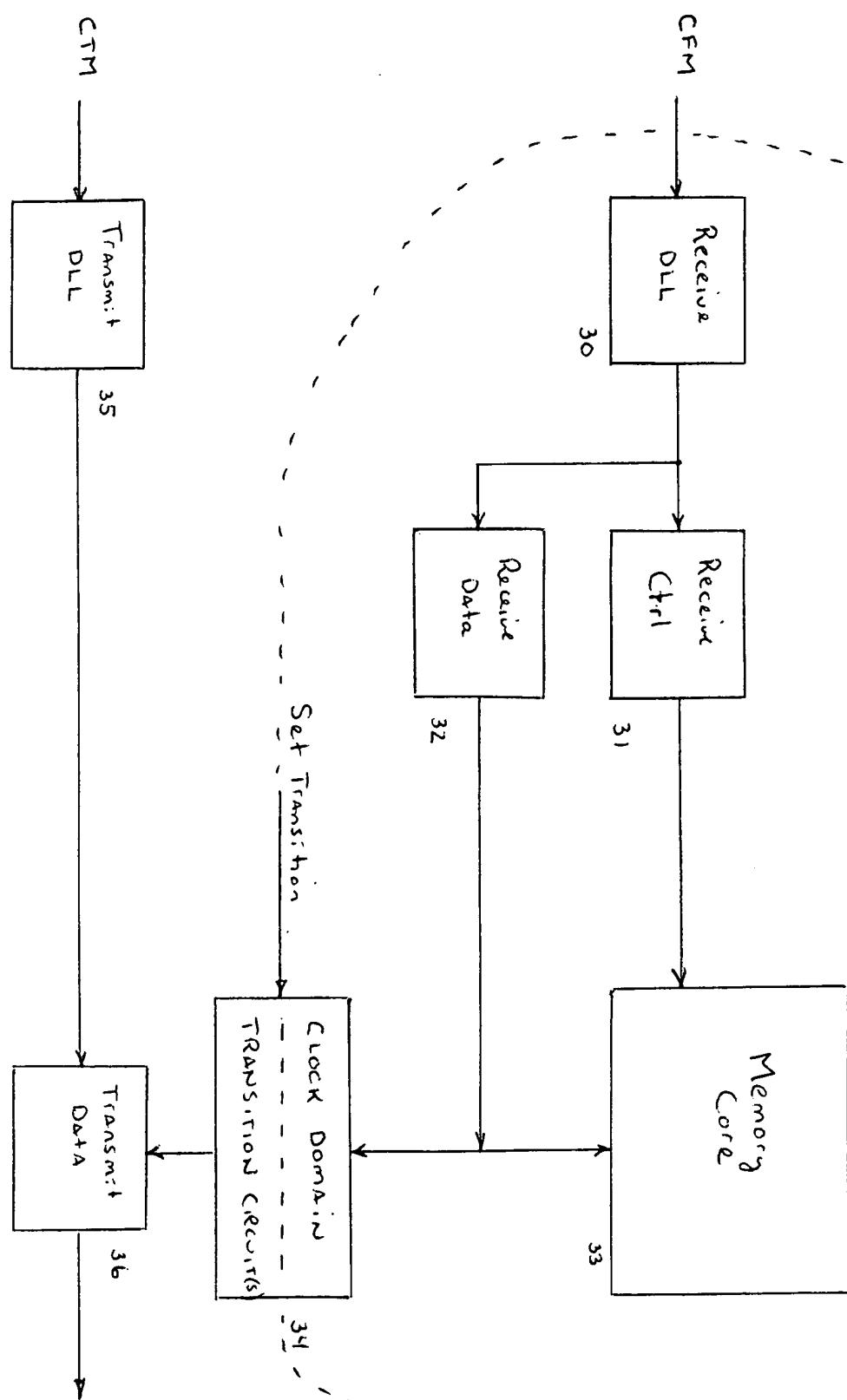


Figure 12

